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IN THE CLAIMS

Kindly amend claims 1, 4-9, and 11 as shown in the following claim listing:

1. (currently amended) An active matrix electroluminescent (EL) display device comprising a matrix array of electroluminescent display elements each of which has an associated switching circuit for controlling the current through the display element in accordance with an applied drive signal, wherein the switching circuit comprises:

a drive transistor and a cascade transistor in series with an associated EL display element, the drive transistor being for driving a current through the associated EL display element and the cascode transistor being connected between the drive transistor and the associated EL display element;

a storage capacitor connected between a power supply line to which the source of the drive transistor is also directly connected and the gate of the drive transistor, for storing a gate voltage for the drive transistor; and

a first switch for allowing or preventing the drive current to flow through the EL display element~~r~~;

wherein the switching circuit is operable in two modes, a first mode in which an input current is sampled by the drive transistor and the first switch is open, and a second mode in which the drive transistor drives a current corresponding to the input current through the EL display element, and the first switch is closed.

2. (original) A device as claimed in claim 1, further comprising a second switch between the gate and drain of the drive transistor.

3. (original) A device as claimed in claim 2, wherein the second switch comprises an n-channel transistor and a p-channel transistor in parallel.

4. (currently amended) A device as claimed in ~~any preceding claim~~ claim 1, 2, or 3 further comprising a third switch between the gate and drain of the cascade transistor.

5. (currently amended) A device as claimed in ~~any preceding claim~~ claim 1, 2, or 3 further comprising a second storage capacitor connected between the gate of the cascade transistor and the power supply line.

6. (currently amended) A device as claimed in ~~any preceding claim~~ claim 1, 2, or 3 further comprising a fourth switch between the drain of the cascade transistor and a current input to the switching circuit.

7. (currently amended) A device as claimed in ~~any preceding claim~~ claim 1, 2, or 3 wherein the first switch is connected between the cascade transistor and the associated display element.

8. (currently amended) A device as claimed in ~~any one of claims 1 to 6~~ claim 1, 2, or 3 wherein the first switch is connected between the associated display element and a second power supply line, which is common to all display elements of the device.

9. (currently amended) A device as claimed in ~~any preceding claim~~ claim 1, 2 or 3 wherein the display elements are arranged in rows and columns, and said switch or switches of the switching circuit for a row of display elements are connected to a respective,

common, row address conductor via which a selection signal for operating the switches in that row is supplied, and each row address conductor is arranged to receive a selection signal in turn, whereby the rows of display elements are addressed one at a time in sequence.

10. (original) A device as claimed in claim 9, wherein the drive signals for the display elements in a column are supplied via a respective column address conductor common to the display elements in the column, the input current being supplied to or drained from the column address conductor.

11. (currently amended) A device according to ~~any preceding claim~~ claim 1, 2, 3, wherein the drive transistor, the cascode transistor and the switch or switches comprise thin film transistors carried on an insulating substrate.